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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP00/04141 06/23/2000

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11/181559 06/28/1999

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature Initials	JAPAN	16	20	9

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## TITLE

Polysilicon semiconductor thin film substrate, method for producing the same, semiconductor device, and electronic device

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